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Notice of Allowability	Application No.	Applicant(s)	
	10/611,788	SIM, JAE-YOON	
	Examiner	Art Unit	
	Michael t Tran	2818	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--
 All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to communications filed June 30, 2003.
2. ☒ The allowed claim(s) is/are 1-57.
3. ☒ The drawings filed on 30 June 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date <u>0603</u> | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |



DETAILED ACTION

1. In response to the Communication dated June 30, 2003, claims 1-57 are active in this application.

Foreign Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a) (d), which papers have been placed of record in the file.

Information Disclosure Statement

3. The information disclosure statement filed June 30, 2003 has been considered.

Allowable Subject Matter

4. Claims 1-57 are allowable over the prior art of record.
5. The following is an Examiner's statement of reasons for the indication of allowable subject matter: the prior art of records does not show (in addition to other elements in the claim) the following:
 - ❖ Wherein the offset-compensated amplifier circuit is structured to compensate an offset voltage with respect to the reference voltage in response to a first control signal before the voltage variation of the first bit line is detected; and wherein a part of the offset-compensated amplifier circuit is disposed at the first region and

a remaining part of the offset-compensated amplifier circuit is disposed at a third region that is different from the first and second regions.

- ❖ Wherein the bias voltage generator circuit and the driver circuit form a differential amplifier; and wherein the driver circuit and the switch are disposed at the second region and the bias voltage generator circuit is disposed at a third region different from the first and second regions.
- ❖ Wherein the bias voltage generator circuit, the first driver circuit, and the third switch circuit form a first differential amplifier, and the bias voltage generator circuit, the second driver circuit, and the third switch circuit form a second differential amplifier; and wherein the first and second driver circuits and the first to third switch circuits are in the second region, and the bias voltage generator circuit is located in a third region that is different from the first and second regions.
- ❖ Wherein the first bias voltage generator circuit and the first driver circuit form a first differential amplifier, and the second bias voltage generator circuit and the second driver circuit form a second differential amplifier; and wherein the first and second driver circuits and the switch circuit are located in the second region, and the first and second bias voltage generator circuits are located in a third region that is disposed at a different place from the first and second regions.
- ❖ Wherein the first bias voltage generator circuit, the first driver circuit, and the third switch circuit form a first differential amplifier, and the second bias voltage generator circuit, the second driver circuit, and the fourth switch circuit form a

second differential amplifier; and wherein the first and second driver circuits and the first to fourth switch circuits are located in the second region, and the first and second bias voltage generator circuits are located in a third region that is different from the first and second regions.

6. Any comments considered necessary by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael T. Tran whose telephone number is (571) 272-1795.

8. Any inquiry of a general nature or relating to the status of this application should be directed to Group receptionist whose telephone number is (571) 272-1650.



Michael T. Tran

June 28, 2004